

## REMARKS

### ***Preliminary Remarks***

The Office Action of June 14, 2005 has been received and its contents carefully analyzed. Claims 1-8 are currently pending, of which claims 1, 3, 7 and 8 stand rejected. New claims 9-15 are added, which are similar to original claims 1-8, but are of different scope. Claim 1 is amended to add a comma before the word "comprising".

Additionally, a replacement sheet is provided for Figure 1 to add the legend "PRIOR ART". A replacement sheet is provided for Figure 2 to show that the feedback current loops from the display panel 23 to the current voltage converter 11, as described at page 7 of the specification. A new sheet is provided for Figure 4 to show the details of the display panel 400, including one or more first electrodes 403, one or more second electrodes 405, one or more light emitting elements 407, and one or more transistors 401. Details of the display controller 10, including the current feedback system, are shown in at least revised Figure 2 and described on at least pages 7-8 of the specification. Additionally, the specification is amended to describe new Figure 4.

These amendments do not add new matter or present any new issues that would require any further consideration and/or search by the Examiner. Reconsideration and withdrawal of all pending objections and rejections in view of the above amendments and following remarks is respectfully requested.

### ***Allowable Subject Matter***

Applicants appreciate the indication that claims 2, 4-6 contain allowable subject matter, but submit that all of the claims are in condition for allowance as currently written.

### **Objection to Drawings**

The drawings were objected to as not showing all the features of the claims and as not including the legend "PRIOR ART" on Figure 1. These objections are moot in view of the replacement sheets for Figures 1 and 2, and the new sheet for Figure 4 that is provided herewith.

### **35 U.S.C. § 102 Rejection**

Claims 1, 3, 7 and 8 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,121,950 to Zavracky, *et al.* ("Zavracky"). This rejection is respectfully traversed.

Claim 1 recites, in pertinent part:

a display panel including a plurality of pixels arranged in a matrix pattern, a plurality of first electrodes individually formed corresponding to the pixels, a second electrode formed in common with the first electrodes, a plurality of light emitting elements provided between the first electrode and the second electrode and including a light emitting layer, and a plurality of transistors provided corresponding to the pixels and connected between the first electrodes and a power supply voltage line for controlling the current supply to the EL elements;

a scan driver for sequentially selecting respective pixel lines;

a data driver for applying an RGB display signal corresponding to a pixel line of the display panel each time the pixel line is selected;  
and

a display controller for using a current value fed back by the second electrode of the display panel and externally input RGB data to correct a white gray level of the RGB data and generate RGB display data, and for providing the generated RGB display data to the data driver,

wherein the display controller determines an amount of emitted light on the corresponding screen according to the fed back current to generate a brightness control reference signal corresponding to the amount of emitted light, and controls the white gray level of the RGB data according to the brightness

control reference signal to control the brightness of the display panel.

The rejection is in error because Zavracky does not disclose each and every element of claim 1.

Zavracky teaches inputting a regulated LCD voltage, analog RGB signals, and signals outputted from a light meter 180 and a temperature measurer 170 to a video polarity network 150, which outputs a positive-white 153b and an inverted negative-white 153a. The signals outputted from the light meter 180 and from the temperature measurer 170 do not originate from a display panel electrode, but rather from light sensors 94 and temperature sensors 92 that are embedded in the active matrix 23 of the LCD. In contrast, claim 1 recites "using a current value fed back by the second electrode of the display panel". The invention's "second electrode of the display panel" is not a light sensor or a temperature sensor as taught by Zavracky. Consequently, Zavracky does not disclose each and every element of claim 1.

Claims 7 and 8 recite features similar those in claim 1 that are discussed above. Additionally, claim 9 recites "using a current value fed back by the display panel", which is different from using signals outputted from temperature and light sensors as taught by Zavracky. Claim 15 recites features similar to those recited in claim 9. Since claims 2-6 depend from claim 1 and since claims 10-14 depend from claim 9, they are also patentable over the cited reference. Thus, claims 1-15 are distinguishable over the cited prior art. The examiner is respectfully requested withdraw the rejection of claims 1 3, 7, and 8, and to pass claims 1-15 to allowance.

### CONCLUSIONS

Applicants submit that a full and complete response has been made to the pending Office Action and respectfully submit that all of the stated objections and/or grounds for rejection have been overcome or rendered moot. Accordingly, Applicants respectfully submit that all pending claims are patentably distinct from the prior art of record and are in condition for allowance. The Examiner is thus respectfully requested to pass the above application to issue.

Should the Examiner feel that there are any issues outstanding after consideration of this Amendment, the Examiner is invited to contact the Applicants' undersigned representative at the number below to expedite prosecution. Prompt and favorable consideration of this Amendment is respectfully requested. Applicants respectfully request that a timely Notice of Allowance be issued for this application.

Respectfully submitted,

  
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